



Application Ser. No. 09/749,725  
Attorney Docket No. 2207/10120  
Assignee: Intel Corporation

AF/61

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

INVENTOR: James BURNS, et al.

SERIAL NO: 09/749,725

FILING DATE: December 28, 2000

TITLE: METHOD AND APPARATUS FOR MAPPING IN ONE STAGE  
AND MERGING AND REMAPPING IN A SECOND STAGE A  
PLURALITY OF INSTRUCTION GROUPS TO A PLURALITY OF  
FUNCTIONAL UNITS (as amended)

GROUP ART UNIT: 2183

EXAMINER: Amol V. Gole

COMMISSIONER FOR PATENTS  
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Alexandria, VA 22313-1450

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**Technology Center 2100**

**RESPONSE TO OFFICE ACTION**

SIR:

In response to the final Office Action mailed June 1, 2004, the Applicant respectfully submits the following.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 6 of this paper.